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The VisSim product family

The VisSim product family includes several base products and product suites, as well as a comprehensive set of targeted add-on modules that address specific problems in areas such as data communications, data acquisition, linearization and analysis, and digital signal processing.

### Base products and product suites

<table>
<thead>
<tr>
<th>Product</th>
<th>Function</th>
</tr>
</thead>
<tbody>
<tr>
<td>Professional VisSim</td>
<td>Model-based design, simulation, testing, and validation of dynamic systems.</td>
</tr>
<tr>
<td></td>
<td>A personal version, VisSim PE, is also available. VisSim PE limits diagram size to 100 blocks.</td>
</tr>
<tr>
<td>VisSim/Comm Suite</td>
<td>Simulates end-to-end communication systems at the signal level using 200+ communications, signal processing, and RF blocks.</td>
</tr>
<tr>
<td></td>
<td>Includes Professional VisSim and VisSim/Comm blockset.</td>
</tr>
<tr>
<td></td>
<td>A personal version, VisSim/Comm Suite PE, is also available. VisSim/Comm PE limits diagram size to 100 blocks and limits the Communication blockset. See the VisSim/Comm datasheet for details.</td>
</tr>
<tr>
<td></td>
<td>VisSim/Comm Suite add-on modules are available for real-time data acquisition (Red Rapids digital tuner card); modeling PCCC turbo codes, including UMTS specification; and for support of Bluetooth, 802.11 a/b/g (Wi-Fi), and ultrawideband wireless designs.</td>
</tr>
<tr>
<td>VisSim/Embedded Controls Developer Suite</td>
<td>Rapidly prototypes and creates embedded controls for DSPs, DSCs, and MSP430 microcontrollers. You can simulate and generate scaled, fixed-point ANSI C code, as well as code for on-chip peripherals.</td>
</tr>
</tbody>
</table>
Includes Professional VisSim, VisSim/C-Code, VisSim/Fixed-Point, and one user-specified target support.

A personal version, VisSim/Embedded Controls Developer PE, is also available. VisSim/Embedded Controls Developer PE limits diagram size to 100.

VisSim Viewer (free) Lets you share VisSim models with colleagues and clients not licensed to use VisSim.

## Add-on modules

<table>
<thead>
<tr>
<th>Add-On Module</th>
<th>Function</th>
</tr>
</thead>
<tbody>
<tr>
<td>VisSim/Analyze</td>
<td>Performs frequency domain analysis of a linearized nonlinear subsystem.</td>
</tr>
<tr>
<td>VisSim/CAN</td>
<td>Interfaces with a USB CAN device to read and write CAN messages on the CAN bus.</td>
</tr>
<tr>
<td>VisSim/C-Code</td>
<td>Generates highly-optimized, ANSI C code that can be compiled and run on any platform that supports an ANSI C compiler.</td>
</tr>
<tr>
<td>VisSim/C-Code Support Library Source</td>
<td>Provides source code for the Support Library.</td>
</tr>
<tr>
<td>VisSim/Comm blockset</td>
<td>Simulates end-to-end communication systems at the signal level using 200+ communications, signal processing, and RF blocks.</td>
</tr>
<tr>
<td></td>
<td>A personal version, VisSim/Comm PE, is also available. VisSim/Comm PE is a subset of the Communication blockset. See the VisSim/Comm datasheet for details.</td>
</tr>
<tr>
<td></td>
<td>You can purchase VisSim/Comm add-on modules for real-time data acquisition (Red Rapids digital tuner cards); for modeling PCCC turbo codes, including UMTS specification; for support of Bluetooth, 802.11 a/b/g (Wi-Fi), and ultrawideband wireless designs.</td>
</tr>
<tr>
<td>VisSim/Digital Power Designer</td>
<td>Provides high-level blocks for simulation and code generation of power supply and digital power components and controls.</td>
</tr>
<tr>
<td>VisSim/Fixed-Point</td>
<td>Simulates the behavior of fixed-point algorithms prior to code generation and implementation of the algorithm on the fixed-point target.</td>
</tr>
<tr>
<td>VisSim/Knobs and Gauges</td>
<td>Provides dynamic gauges, meters, and knobs for process control, and measurement and validation systems.</td>
</tr>
<tr>
<td>VisSim/Model-Wizard</td>
<td>Generates transfer function model from historic or real-time data.</td>
</tr>
<tr>
<td>VisSim/Motion</td>
<td>Simulates motor control systems with customizable amplifiers, controllers, filters, motors, sensors, sources, tools, and transforms.</td>
</tr>
<tr>
<td>VisSim/Neural-Networks</td>
<td>Performs nonlinear system identification, problem diagnosis, decision-making prediction, and other problems where pattern recognition is important.</td>
</tr>
<tr>
<td>VisSim/OPC</td>
<td>Connects to any OPC server and log data or run a virtual plant in VisSim for offline tuning.</td>
</tr>
<tr>
<td>VisSim/OptimizePRO</td>
<td>Performs generalized reduced gradient method of</td>
</tr>
<tr>
<td>VisSim/Real-TimePRO</td>
<td>Performs real-time data acquisition and signal generation using I/O cards, PLCs, and DCSs.</td>
</tr>
<tr>
<td>---------------------</td>
<td>--------------------------------------------------------------------------------------------</td>
</tr>
<tr>
<td>VisSim/Serial</td>
<td>Performs serial I/O with other computers.</td>
</tr>
<tr>
<td>VisSim/State Charts</td>
<td>Creates, edits, and executes event-based systems.</td>
</tr>
<tr>
<td>VisSim/UDP</td>
<td>Performs data exchange over the internet using UDP.</td>
</tr>
<tr>
<td>VisSim Viewer (free)</td>
<td>Lets you share VisSim models with colleagues and clients not licensed to use VisSim.</td>
</tr>
</tbody>
</table>

## Resources for learning VisSim/Serial

For those of you that are new to VisSim, we have provided several free services to make your transition to VisSim fast, smooth, and easy:

### Interactive webinars

Interactive webinars offer you the opportunity to meet with Altair product specialists who will introduce and demonstrate our software products live on your computer and answer any questions you have. Each webinar is approximately 45 minutes long. To learn more about our interactive webinars, go to [http://www.vissim.com/webinars/webinars.html](http://www.vissim.com/webinars/webinars.html).

### Sample diagrams

VisSim 9.0 includes a directory of fully documented sample diagrams. These diagrams illustrate both simple and complex models spanning a broad range of engineering disciplines, including aerospace, biophysics, chemical engineering, control design, dynamic systems, electromechanical systems, environmental systems, HVAC, motion control, process control, and signal processing.

*To access sample diagrams*

Click on the **Diagrams** menu in VisSim.

Click on **Examples > Applications**.

### Training

Altair offers training sessions for learning and gaining expertise in VisSim and the VisSim family of add-on products. Training sessions are conducted at Altair training facility in Westford, MA, as well as at customer sites and as online webinars.

For information on setting up a training session, contacts sales@vissol.com.
Installing VisSim/UDP

This section contains…

Installation procedure

To install VisSim/UDP

- Run setupVisSimUDP90.exe.

At the completion of the installation the VisSim/UDP add-on will appear in the Addons window for the Edit > Preferences > Addons command, and the VisSim/UDP blocks will appear in the Blocks > Real-Time menu.

UDP device driver

VisSim/UDP works with the standard ethernet device. It uses the native WinSock drivers. No additional driver installation is required.
Using VisSim/UDP

This section contains…

**UDP Read block**

The UDP Read block reads data from the UDP bus.

**IP address:** Specifies IP address or Host name of target.

**UDP Port:** Specifies the port to exchange data. It recommended to use ports > 49151.

**Data Pins** Specifies the number of input pins (128 max).

**Packet Byte Size:** Specifies the size of packet to be read. (512 bytes max).
**Data Element Type:** Controls data type of each output pin, and offset into packet.

**Pack Offsets:** Iterates over all pins and assigns consecutive ascending offsets to each pin.

**Set All Pin Types = 1:** Sets data type of all pins to be same as pin 1, and then does a Pack Offset.

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**UDP Write block**

The UDP Write block writes data to the UDP port on the ethernet. The top “Tx” pin must have the value 1 in order for the block to send data. The subsequent pins are data pins. The values presented on the data pins sent to the UDP port.

**VisSim UDP Write Block Properties**

- **Host Name or IP address:** Specifies IP address or Host name of target.
- **UDP Port:** Specifies the port to exchange data. It recommended to use ports > 49151.
- **Data Pins:** Specifies the number of input pins (128 max).
- **Packet Byte Size:** Specifies the size of packet to be written (512 bytes max).
- **Data Element Type:** Controls data type of each output pin, and offset into packet.
- **Pack Offsets:** Iterates over all pins and assigns consecutive ascending offsets to each pin.
- **Set All Pin Types = Pin 1:** Sets data type of all pins to be same as pin 1, and then does a Pack Offset.
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